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DINSMORE & SHOHL LLP			EXAMINER	
ONE DAYTON CENTRE, ONE SOUTH MAIN STREET			DICKEY, THOMAS L	
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Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary	Application No. 10/575,754	Applicant(s) SUBRAMANYAM ET AL.
	Examiner Thomas L. Dickey	Art Unit 2826

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
 - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
 - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED. (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on **31 July 2009**.
 2a) This action is **FINAL**. 2b) This action is non-final.
 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) **43-64 and 66-75** is/are pending in the application.
 4a) Of the above claim(s) **55-58,61-64 and 66-75** is/are withdrawn from consideration.
 5) Claim(s) _____ is/are allowed.
 6) Claim(s) **43-54,59 and 60** is/are rejected.
 7) Claim(s) _____ is/are objected to.
 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.
 10) The drawing(s) filed on **13 April 2006** is/are: a) accepted or b) objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) Notice of References Cited (PTO-892)
 2) Notice of Draftsperson's Patent Drawing Review (PTO-948)
 3) Information Disclosure Statement(s) (PTO/SB/08)
 Paper No(s)/Mail Date **07/31/2009**.
- 4) Interview Summary (PTO-413)
 Paper No(s)/Mail Date. _____.
 5) Notice of Informal Patent Application
 6) Other: _____

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DETAILED ACTION

1. The amendment filed on 07/23/2009 has been entered.

Information Disclosure Statement

2. The Information Disclosure Statement filed on 07/31/2009 has been considered.

Claim Rejections - 35 USC § 103

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 43-54, 59, and 60 are rejected under 35 U.S.C. 103(a) as being unpatentable over KUYLENSTIERNA ET AL. ("Tunable Electromagnetic Bandgap Structures based on Ba_{.25}Sr_{.75}TiO₃ Parallel-plate Varactors on Silicon Coplanar Waveguides", 33rd European Microwave Conference, 2003) in view of Satoh et al. (5,576,564). In the examiner's opinion, this/these claim(s) would have been obvious according to one of the rationales expressed in the *Examination Guidelines for Determining Obviousness Under*

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35 U.S.C. 103 in View of the Supreme Court Decision in *KSR International Co. v. Teleflex Inc.*, as published at 72 Federal Register 57526 et seq.¹ (10/10/2007).

The Guidelines explain that an invention that would have been obvious to a person of ordinary skill at the time of the invention is not patentable. The Guidelines point out that, as reiterated by the Supreme Court in KSR, the framework for the objective analysis for determining obviousness under 35 U.S.C. 103 is stated in *Graham v. John Deere Co.* Obviousness is a question of law based on underlying factual inquiries. The factual inquiries enunciated by the Court are as follows:

- (1) Determining the scope and content of the prior art;
- (2) Ascertaining the differences between the claimed invention and the prior art, and
- (3) Resolving the level of ordinary skill in the pertinent art.

Examining this last factor first, it is noted that any obviousness rejection should include, either explicitly or implicitly in view of the prior art applied, an indication of the level of ordinary skill. This is an essential finding because (as the Guidelines point out) a finding as to the level of ordinary skill may be used as a partial basis for a resolution of the issue of obviousness. The person of ordinary skill in the art is a hypothetical person

¹ Available at <http://www.uspto.gov/web/offices/com/sol/notices/72fr57526.pdf> See also MPEP, Eighth Ed. Rev. 6 (Sept. 2007) §§ 2141 et seq., available at <http://www.uspto.gov/web/offices/pac/mpep/documents/2100.htm> Please note that §§ 2141 et seq. have been completely revised, in view of KSR v. Teleflex.

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who is presumed to have known the relevant art at the time of the invention. Factors that may be considered in determining the level of ordinary skill in the art include:

- (1) "Type of problems encountered in the art;"
- (2) "prior art solutions to those problems;"
- (3) "rapidity with which innovations are made;"
- (4) "sophistication of the technology;" and
- (5) "educational level of active workers in the field."

In a given case, every factor may not be present, and one or more factors may predominate. In the present case, Applicant has presented claims to a device classified in Class 257 (Semiconductor Devices). Considering the five enumerated factors in order, it is noted that:

- 1) The types of problems encountered in Class 257 typically are highly complex, involving questions of electrodynamics, thermodynamics, crystallography, and quantum mechanics.
- 2) Prior art solutions to the problems presented in this field demonstrate thinking of the highest order. Many prior art solutions in this field have won Nobel prizes. Over a half century or more, the Nobel Prize Committee has recognized² the extraordinary nature (and high value, both commercially and in terms of quality of

² For example, see "Presentation Speech, 2000 Nobel Prize,
http://nobelprize.org/nobel_prizes/physics/laureates/2000/presentation-speech.html

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life) of prior art solutions to complex problems in this field made by individuals such as John Bardeen, William Shockley, Jack Kilby, Leo Esaki, Nick Basow, Zhores Alferov, Pierre-Gilles de Gennes, Herbert Kroemer, and perhaps a few more this writer has overlooked. Note, for example, that the most recent Nobel Prize in physics went to Albert Fert and Peter Grünberg for an innovative solution to the problem of Giant Magnetic Resonance, a solution now incorporated into many semiconductor memory devices.

- 3) Innovations in Class 257 are made with extremely high rapidity (see, e.g. "Moore's Law").
- 4) Technology used to make and practice inventions in this field are highly sophisticated. Some "fabs" (as those of skill in the art call the factories for making these devices) now cost in excess of six billion dollars each³, and perform literally hundreds of billions of operations per hour.
- 5) Finally, the educational level of active workers in this field is extremely high – Ph.D.s are common, and a bachelor's degree in engineering is the absolute minimum educational level of workers in this field.

In short, the level of ordinary skill in this field is extremely high. In *KSR* (while considering an invention involving the substitution of one simple mechanical linkage for an-

³ See, e.g., "Trying to Put New Zip Into Moore's Law", *New York Times*, 2/24/2008

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other), the Supreme Court cautioned, "A person of ordinary skill is also a person of ordinary creativity". *KSR Int'l Co. v. Teleflex Inc.*, 127 S.Ct. 1727, 1742, 82 USPQ2d 1385, 1397 (2007). Had the Court been looking at 1) the scientific complexity of problems encountered in the semiconductor arts, 2) the variety of extraordinarily valuable (from lifestyle-changing, such as high-speed communications and computing, to handy devices such as IPods and cellphones) and difficult solutions to challenging problems that have been accomplished in the semiconductor art in recent years, 3) the extreme rapidity of advances in this art, 4) the highly sophisticated "fabs" used to build semiconductor devices, and 5) the high educational level of persons practicing this art, the Court might easily have said that in the semiconductor art the person of ordinary skill is a person of extraordinary creativity.

Next, we consider the first and second factual findings required by *Graham*. The scope and content of the prior art includes, in the Kuylenstierna et al. disclosure, a description of a varactor shunt switch for microwave applications, the varactor shunt switch comprising: a high resistivity silicon layer having a resistivity greater than 1 k Ω -cm and a thickness of about 0.051 cms; a 200 nm thick silicon oxide layer on said high resistivity silicon layer; a 500 nm thick metallic layer, comprising a 400 nm thick gold layer on said silicon oxide layer and a 100 nm thick platinum layer on said gold layer, on

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said silicon oxide layer; a tunable ferroelectric thin-film dielectric layer on said metallic layer, wherein said tunable ferroelectric thin-film dielectric layer has a dielectric constant of greater or equal to about 200 at zero bias, an optimized dielectric constant of 1200, and a thickness of greater than 250 nm; and a top metal electrode on said tunable ferroelectric thin-film dielectric layer, wherein said top metal electrode defines a coplanar waveguide transmission line. Note figure 1 and pages 1111 (column 2) through 1112 (column 1) of Kuylensierna et al. Note particularly figure 1(b), showing that top metal electrode defining the coplanar waveguide transmission line comprises at least two ground conductors (labeled "ground plane" in the figure) and a central signal strip.

The applicants' claims 53, 54, 59, and 60 do not distinguish over the Kuylensierna et al. reference regardless of the process used to form the metallic layer, because only the final product is relevant, not the recited processes of depositing and lifting off said metallic layer by RF sputtering, pulsed layer deposition, or electron beam deposition and standard lift-off photolithography. See *SmithKline Beecham Corp. v. Apotex Corp.*, 78 USPQ2d 1097 (Fed. Cir, 2006) ("While the process set forth in the product-by-process claim may be new, that novelty can only be captured by obtaining a process claim.")

Note that when "product by process" claiming is used to describe one or more limitations of a claimed product, the limitations so described are limitations of the claimed

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product per se, no matter how said product is actually made. *In re Hirao*, 190 USPQ 15 at 17 (footnote 3). See also *In re Brown*, 173 USPQ 685; *In re Luck*, 177 USPQ 523; *In re Fessmann*, 180 USPQ 324; *In re Avery*, 186 USPQ 161; *In re Wertheim*, 191 USPQ 90 (209 USPQ 554 does not deal with this issue); and *In re Marosi et al.*, 218 USPQ 289, all of which make it clear that it is the patentability of the final product per se which must be determined in a "product by process" claim and not the patentability of the process, and that an old or obvious product produced by a new method is not patentable as a product, whether claimed in "product by process" claims or not. Note that applicant has the burden of proof in such cases, as the above caselaw makes clear. See also MPEP 706.03(e).

The Federal Circuit recently revisited the question of whether a "product by process" claim can be anticipated by a reference that does not recite said process. See *Smith-Kline Beecham Corp. v. Apotex Corp.*, 78 USPQ2d at 1100. The Federal Circuit cited with approval this Office's current statement of the law, found in MPEP § 2113:

[Even] though product-by-process claims are limited by and defined by the process, determination of patentability is based on the product itself. The patentability of a product does not depend on its method of production. If the product in the product-by-process claim is the same as or obvious from a product of the prior art, the claim is unpatentable even though the prior product was made by a different process.

Id. at 1101. The Federal Circuit held this statement to be consistent with its own views on this topic, as well as various Supreme Court rulings, notably *Gen. Elec. Co. v. Wabash Appliance Corp.*, 304 U.S. 364, 373 (1938) ("Although in some instances a

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claim may validly describe a new product with some reference to the method of production, a patentee who does not distinguish his product from what is old except by reference, express or constructive, to the process by which he produced it, cannot secure a monopoly on the product by whatever means produced."). Id.

Once the examiner produces a *prima facie* case, the burden shifts to the applicant, "to prove that the prior art products do not necessarily or inherently possess the characteristics of his claimed product". *In re Thorpe*, 227 USPQ 964,966 (Fed. Cir. 1985), quoting *In re Fitzgerald*, 619 F.2d 67, 70, 205 USPQ 594,596 (CCPA 1980). See also *In re Best*, 562 F.2d 1252,1255, 195 USPQ 430,433-34 (CCPA 1977), and *In re Brown*, 59 CCPA 1036, 1041, 459 F.2d 531, 535, 173 USPQ 685, 688 (1972), where the court explains the reasoning behind this rule: "[W]hen the prior art discloses a product which reasonably appears to be either identical with or only slightly different than a product claimed in a product-by-process claim, a rejection based alternatively on either section 102 or section 103 of the statute is eminently fair and acceptable. As a practical matter, the Patent Office is not equipped to manufacture products by the myriad of processes put before it and then obtain prior art products and make physical comparisons therewith" (emphasis added).

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The difference between the prior art and the claimed device is that the claim requires a combination of Kuylensierna et al.'s varactor shunt switch with a 20 nm thick titanium adhesion layer formed between the silicon oxide layer and the metallic layer.

However, Satoh et al. discloses a varactor with a 20 nm thick titanium adhesion layer 3 formed between a silicon oxide layer 2 and a metallic layer 4. Note figure 1 and column 4 lines 42-50 of Satoh et al. The question is, taking into account the high level of education, skill, and creativity of one of ordinary skill in the semiconductor art, would it have been obvious to combine Satoh et al.'s 20 nm thick titanium adhesion layer with Kuylensierna et al.'s varactor shunt switch?

To reject a claim based on the basis of the rationale expressed in section IIIA of the *Examination Guidelines*, Office personnel first must resolve the Graham factual inquiries (as has just been done). Office personnel must then articulate the following:

- (1) a finding that the prior art included each element claimed, although not necessarily in a single prior art reference, with the only difference between the claimed invention and the prior art being the lack of actual combination of the elements in a single prior art reference;
- (2) a finding that one of ordinary skill in the art could have combined the elements as claimed by known methods, and that in combination, each element merely would have performed the same function as it did separately;
- (3) a finding that one of ordinary skill in the art would have recognized that the results of the combination were predictable; and
- (4) whatever additional findings based on the *Graham* factual inquiries may be necessary, in view of the facts of the case under consideration, to explain a conclusion of obviousness.

As explained above, Kuylensierna et al. discloses a device (varactor shunt switch) which differed from the claimed device by the absence of some components (a 20 nm

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thick titanium adhesion layer formed between the silicon oxide layer and the metallic layer). Because Satoh et al. discloses the missing components, the only difference between the claimed invention and the prior art is the lack of actual combination of varactor shunt switch and a 20 nm thick titanium adhesion layer formed between the silicon oxide layer and the metallic layer in a single prior art reference. Further, Satoh et al. discloses that those of skill in the art were familiar with a method of combining a 20 nm thick titanium adhesion layer formed between a silicon oxide layer and a metallic layer with a ferroelectric varactor very similar to Kuylenstierna et al.'s varactor shunt switch. From the similarities between Satoh et al.'s ferroelectric varactor and Kuylenstierna et al.'s varactor shunt switch, one of skill in the art would have been able to conclude that Satoh et al.'s 20 nm thick titanium adhesion layer could have been combined with the disclosed elements of Kuylenstierna et al.'s varactor shunt switch. One of skill in the art would have had reason to predict (based on its functioning in combination with Satoh et al.'s ferroelectric varactor) that the 20 nm thick titanium adhesion layer, formed between the silicon oxide layer and the metallic layer of Kuylenstierna et al.'s varactor shunt switch, would have continued functioning much as it did while formed between the silicon oxide layer and the metallic layer of Satoh et al.'s ferroelectric varactor, and that when substituted, Kuylenstierna et al.'s varactor shunt switch would continue functioning in the manner disclosed by Kuylenstierna et al. It would therefore have been obvious

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to a person having skill in the art to combine the 20 nm thick titanium adhesion layer taught by Satoh et al. with Kuylensierna et al.'s varactor shunt switch.

Response to Arguments

4. Applicant's arguments with respect to claims 43-54, 59, and 60 have been considered but are moot in view of the new ground(s) of rejection.

Conclusion

5. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thomas L. Dickey whose telephone number is 571-272-1913. The examiner can normally be reached on Monday-Thursday 8-6.

If attempts to reach the examiner by telephone are unsuccessful, please contact the examiner's supervisor, Sue A. Purvis, at 571-272-1236. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

*/Thomas L. Dickey/
Primary Examiner
Art Unit 2826*